

Abstract

A first solid state imaging device (20a) captures a first picture of a subject to generate a first image signal $Y_a(t)$. A
5 second solid state imaging device (20b) captures a second picture of the subject to generate a second image signal $Y_b(t)$. In synchronization with operation of the first and second solid state imaging devices (20a, 20b), a selection circuit (26) alternately selects one of the first and second image signals $Y_a(t)$ and $Y_b(t)$
10 to output a selected image signal. A digital processing circuit (29) includes a first register (33a) that stores first exposure data ED_a generated in accordance with the first image signal $Y_a(t)$, and includes a second register (33b) that stores second exposure data ED_b generated in accordance with the second image signal $Y_b(t)$.
15 This enables smooth switching of operation between the solid state imaging devices.